



# Electronic Filing System (EFS) Data

## Electronic Patent Application Submission

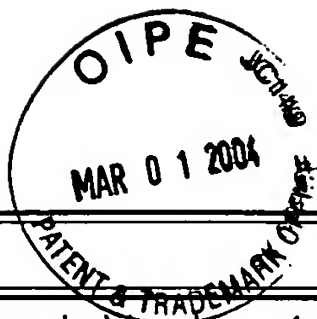
### USPTO Use Only

EFS ID: 56323  
Application ID: 10688710  
Title of Invention: Method of Making an Integrated Circuit Device  
First Named Inventor: Thomas Glenn  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2003-10-17  
Effective Receipt Date: 2004-03-01  
Submission Type: Information Disclosure Statement  
Filing Type:  
Confirmation number: 2199  
Attorney Docket Number: AMKOR007GC1



Total Fees Authorized:


Digital Certificate Holder: cn=Mark B. Garred,ou=Registered Attorneys,ou=Patent and Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US  
Certificate Message Digest: 5a521600c1c39e4da44823f5e58f8f407cf430e3



## TRANSMITTAL

Electronic Version v1.1

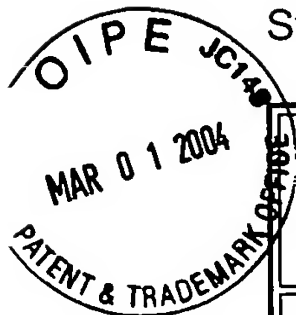
Stylesheet Version v1.1.0

Title of Invention	Method of Making an Integrated Circuit Device	
Application Number: 10/688710 		
Date: 2003-10-17		
First Named Applicant: Thomas P. Glenn		
Confirmation Number: 2199		
Attorney Docket Number: AMKOR007GC1		
<p>I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.</p> <p>I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.</p>		
Submitted by:	Elec. Sign.	Sign. Capacity
Mark B. Garred Registered Number: 34,823	/mbg/	Attorney
Documents being submitted	Files	
us-ids	ids3-usidst.xml	
	us-ids.dtd	
	us-ids.xsl	
Comments		

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0



Title of  
Invention

Method of Making an Integrated Circuit Device

Application Number: 10/688710



Confirmation Number: 2199

First Named Applicant: Thomas Glenn

Attorney Docket Number: AMKOR007GC1

Art Unit: 2811

Search string: ( 5640047 or 5639990 or 5641997 or 5644169  
or 5643433 or 5646831 or 5650663 or 5661088  
or 5665996 or 5673479 or 5683806 or 5689135  
or 5696666 or 5701034 or 5703407 or 5710064  
or 5724233 or 5723899 or 5736432 or 5745984  
or 5753977 or 5753532 or 5766972 or 5770888  
or 5776798 or 5783861 or 5801440 or 5814884  
or 5814883 or 5814881 or 5814877 or 5818105  
or 5817540 or 5821615 or 5821457 or 5835988  
or 5834830 or 5844306 or 5856911 or 5859471  
or 5866939 or 5871782 or 5874784 or 5877043  
or 5886398 or 5886397 or 5894108 or 5897339  
or 5900676 or 5903050 ).pn.

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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	2	5639990	1997-06-17	NISHIHARA ET AL.			
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	4	5644169	1997-07-01	CHUN			
	5	5643433	1997-07-01	FUKASE ET AL.			
	6	5646831	1997-07-08	MANTEGHI			
	7	5650663	1997-07-22	PARTHASARANTHI			
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	9	5665996	1997-09-09	WILLIAMS ET AL.			
	10	5673479	1997-10-07	HAWTHORNE			

	11	5683806	1997-11-04	SAKUMOTO ET AL.
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	40	5859471	1999-01-12	KURAISHI ET AL
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	43	5874784	1999-02-23	AOKI ET AL.
	44	5877043	1999-03-02	ALCOE ET AL.
	45	5886398	1999-03-23	LOW ET AL.
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	48	5897339	1999-04-27	SONG ET AL.
	49	5900676	1999-05-04	KWEON ET AL.
	50	5903050	1999-05-11	THURAIRAJARATNAM ET AL.

Signature

Examiner Name	Date